



# **Intel® Desktop Board D865PCD Specification Update**

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Order Number: C74009-005

The Intel® Desktop Board D865PCD may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

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The Intel® desktop board D865PCD may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

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## REVISION HISTORY

<b>Date of Revision</b>	<b>Version</b>	<b>Description</b>
May 2004	-001	This document is the first Specification Update for the Intel® Desktop Board D865PCD.
July 2004	-002	Added Specification Clarification 1.
August 2004	-003	Added Erratum 1.
November 2004	-004	Added Erratum 2.
December 2004	-005	Added Documentation Change 1.

## PREFACE

This document is an update to the specifications contained in the *Intel® Desktop Board D865PCD Technical Product Specification* (Order number C73314). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

Refer to the *Intel® Pentium® 4 Processor Specification Update* (Order number 249199) for specification updates concerning the Intel Pentium 4 processor and that may apply to the desktop board D865PCD. Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® 865PE/P Chipset Specification Update* (Order Number 252524) for specification updates concerning the 82865P MCH Controller and that may apply to the desktop board D865PCD. Unless otherwise noted in this document, it should be assumed that any MCH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® 82801EB I/O Controller Hub 5 (ICH5) Specification Update* (Order Number 252517) for specification updates concerning the 82801ER I/O Controller Hub and that may apply to the desktop board D865PCD. Unless otherwise noted in this document, it should be assumed that any ICH 5 errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

## Nomenclature

**Specification Changes** are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

**Errata** are design defects or errors. Characterized errata may cause the desktop board D865PCD's behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

**Specification Clarifications** describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

**Documentation Changes** include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

**Specification Update for the  
Intel® Desktop Board D865PCD**





## GENERAL INFORMATION

**Basic Desktop Board D865PCD Identification Information**

AA Revision	BIOS Revision	Notes
C69211-401	RC86510A.86A.0034.P13	1-5
C69211-402	RC86510A.86A.0037.P15	1-5
C69211-403	RC86510A.86A.0037.P15	1-5
C69211-404	RC86510A.86A.0037.P15	1-5
C69863-400	RC86510A.86A.0034.P13	1-5
C69863-401	RC86510A.86A.0037.P15	1-5
C69863-402	RC86510A.86A.0037.P15	1-5

**NOTES:**

1. The AA number is found on a small label on the component side of the board.
2. The 82865P Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping	S-Spec Numbers
82865P HT MCH	A2	SL6UY
82801EB ICH	A3	SL73Z

3. Refer to the *Intel® Pentium® 4 Processor Specification Update* (Order Number 249199) for errata related to the Pentium 4 processor and that may apply to the desktop board D865PCD.
4. Refer to the *Intel® 865PE/P Specification Update* (Order Number 252524) for errata related to the 82865P MCH that may apply to the desktop board D865PCD.
5. Refer to the *Intel® 82801EB I/O Controller Hub 5 (ICH5) Specification Update* (Order Number 252517) for errata related to the 82801ER I/O Controller Hub that may apply to the desktop board D865PCD.

## Summary Table of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board D865PCD. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted. This table uses the following notations:

### CODES USED IN SUMMARY TABLE

Doc:	Document change or update that will be implemented.
Plan Fix:	This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
No Fix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

NO.	PLANS	ERRATA
1	Plan Fix	Some systems may hang if the mouse is moved when entering standby mode
2	Plan Fix	System may intermittently hang at POST code 75 with SATA hard disk drives attached
NO.	PLANS	SPECIFICATION CLARIFICATIONS
1	Doc	Change to description of Table 17, Front Panel Audio Connector
NO.	PLANS	DOCUMENTATION CHANGES
1	Doc	Change to Section 2.8.2.2, Table 17, Front Panel Audio Connector

## ERRATA

### 1. ***Some Systems May Hang if the Mouse is Moved When Entering Standby Mode***

**PROBLEM:** Moving the mouse after placing the system into standby with the mouse may cause the system to immediately awaken and hang.

**IMPLICATION:** The system may hang if the user is moving the mouse directly after using the mouse to place the system into standby mode.

**WORKAROUND:** None.

**STATUS:** This erratum may be fixed in a future board revision.

### 2. ***System May Intermittently Hang at POST Code 75 with SATA Hard Disk Drives Attached***

**PROBLEM:** An intermittent hang at POST code 75 may be observed when SATA Hard Disk Drives are used. This intermittent hang is due to an erroneous circuit race condition in the system clock when spread spectrum is enabled by BIOS.

**IMPLICATION:** The system hang is exhibited when the system clock's internal circuitry is erroneously enabled when spread spectrum is enabled on PLL1.

**WORKAROUND:** The user will need to reset the system, which allows the SATA Hard Disk Drives to be reinitialized.

**STATUS:** This issue may be fixed in a future BIOS revision.

## SPECIFICATION CLARIFICATIONS

The Specification Clarifications listed in this section apply to the *Desktop Board D865PCD Technical Product Specification* (Order Number C73314). All Specification Clarifications will be incorporated into a future version of that specification.

### 1. **Change to Description of Table 17, Front Panel Audio Connector**

Table 17, Front Panel Audio Connector will change in its entirety as follows:

**Table 17. Front Panel Audio Connector**

Pin	Signal Name	Pin	Signal Name
1	Mono Mic in (Stereo Mic 1)	2	Ground
3	Mono Mic Bias (Stereo Mic 2)	4	+5 V
5	RIGHT_OUT	6	Right channel return
7	No connect	8	Key
9	LEFT_OUT	10	Left channel return

## DOCUMENTATION CHANGES

The Documentation Changes listed in this section apply to the *Intel® Desktop Board D865PCD Technical Product Specification* (Order Number C73314). All Documentation Changes will be incorporated into a future version of that specification.

### 1. **Change to Section 2.8.2.2, Table 17, Front Panel Audio Connector**

Table 17 in Section 2.8.2.2 will be replaced in its entirety as follows:

**Table 17. Front Panel Audio Connector**

Pin	Signal Name	Pin	Signal Name
1	Mono Mic in	2	Ground
3	Mono Mic Bias	4	+5 V
5	RIGHT_OUT	6	Right channel return
7	No connect	8	Key
9	LEFT_OUT	10	Left channel return